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JDS
J. White
6-21-02

In re the Application of:

Allowed: **November 6, 2001**

Norio FUKASAWA et al.

Confirmation No. **4720**

Serial No.: **09/635,124**

Examiner: **David E. Graybill**

Filed: **August 8, 2000**

Art Unit: **2814**

For: **METHOD AND MOLD FOR MANUFACTURING SEMICONDUCTOR DEVICE
AND METHOD FOR MOUNTING THE DEVICE**

INFORMATION DISCLOSURE STATEMENT
PURSUANT TO 37 CFR 1.97(b)(4)

Commissioner for Patents
Washington, D.C. 20231

February 6, 2002

Sir:

The attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached Form PTO-1449. One copy of each of these documents is attached.

No fee or certification is required in connection with this Information Disclosure Statement, since it is being submitted prior to the issuance of a first official action on the merits following the **Request for Continued Examination (RCE)** in the above-identified patent application.

The above information is presented so that the Patent and Trademark Office can, in the first instance, determine any materiality thereof to the claimed invention. See 37 CFR 1.104(a) concerning the PTO duty to consider and use any such information. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the documents cited in the attached Form PTO-1449 be made of record therein and appear on the first page of any patent to issue therefrom.

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The Commissioner is authorized to charge our Deposit Account No. 01-2340 for any fee which is deemed by the Patent and Trademark Office to be required to effect consideration of this statement.

Respectfully submitted,

ARMSTRONG, WESTERMAN & HATTORI, LLP



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Enclosures: PTO-1449, and 2 references